

Sohn Joohyeong

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EDUCATION

Mar. 2020 ~ Present	Konkuk University Department of Electrical and Electronics Engineering <i>Bachelor Student</i> GPA: 4.28 / 4.5	Seoul, Korea
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RESEARCH INTERESTS

- SoC design

PROJECT

- A Implementation of an SDF (Single-path Delay Feedback) FFT accelerator utilizing FPGA and AMBA AXI bus
- A Implementation of an MB (Memory-Based) FFT accelerator utilizing FPGA and AMBA AXI bus
- A Implemetation of and PLL Lock Detector module and test bench utilizing the Vivado Simulation tool

AWARDS AND HONORS

- Dean`s List, Konkuk University, Korea (Jul. 2023)
- Dean's List, Konkuk University, Korea (Jan. 2024)
- Dean's List, Konkuk University, Korea (Aug. 2024)
- 운해장학금, 운해장학재단, Korea (Feb. 2024)
- 운해장학금, 운해장학재단, Korea (Feb. 2025)

SKILLS AND TECHNIQUES

- C, Python, Verilog HDL

LANGUAGE SKILL

- English: TOEFL 91(Intermediate Level)